

Figure 1a

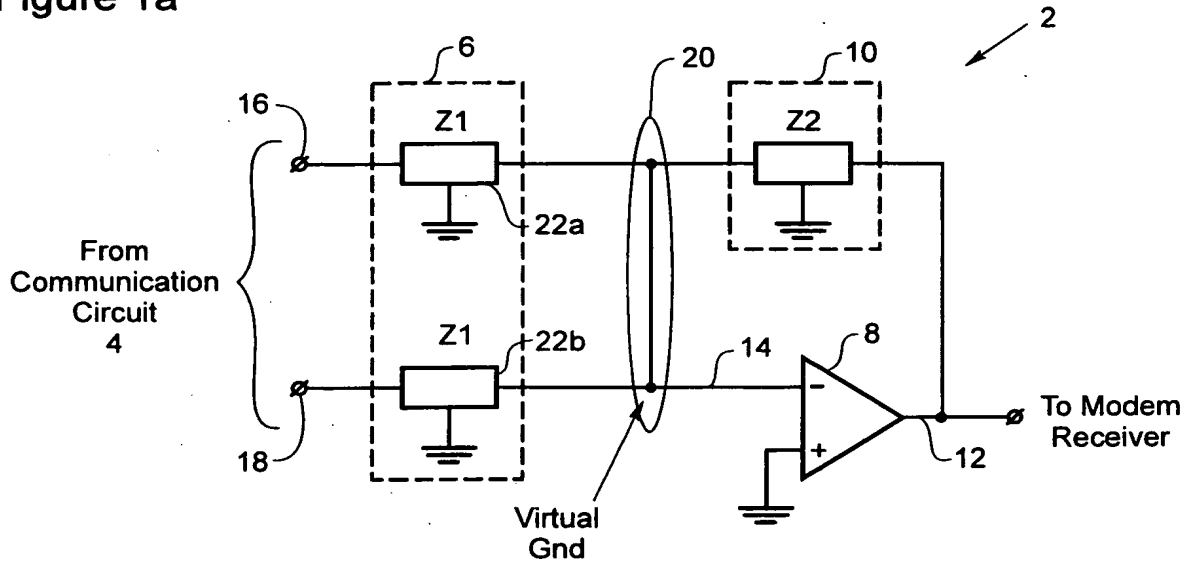


Figure 1b

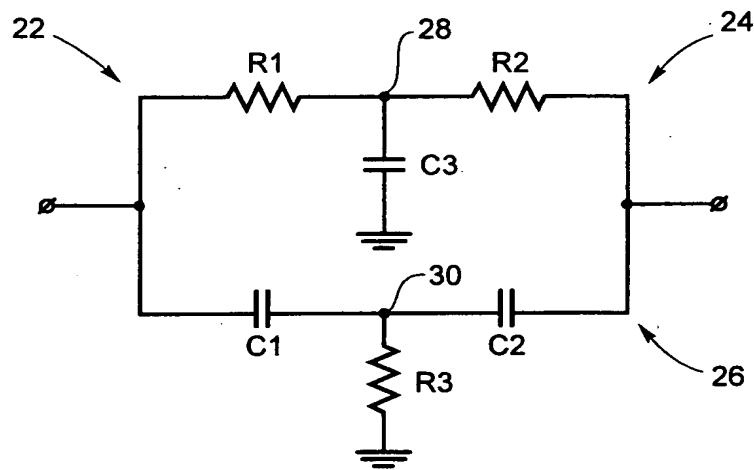


Figure 1c

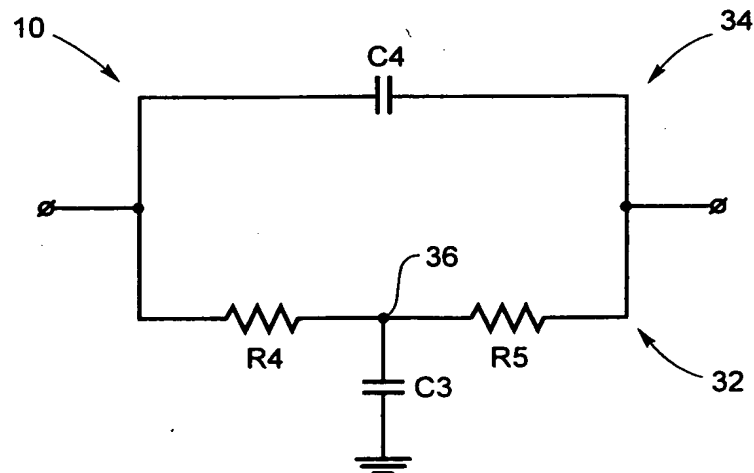


Figure 2

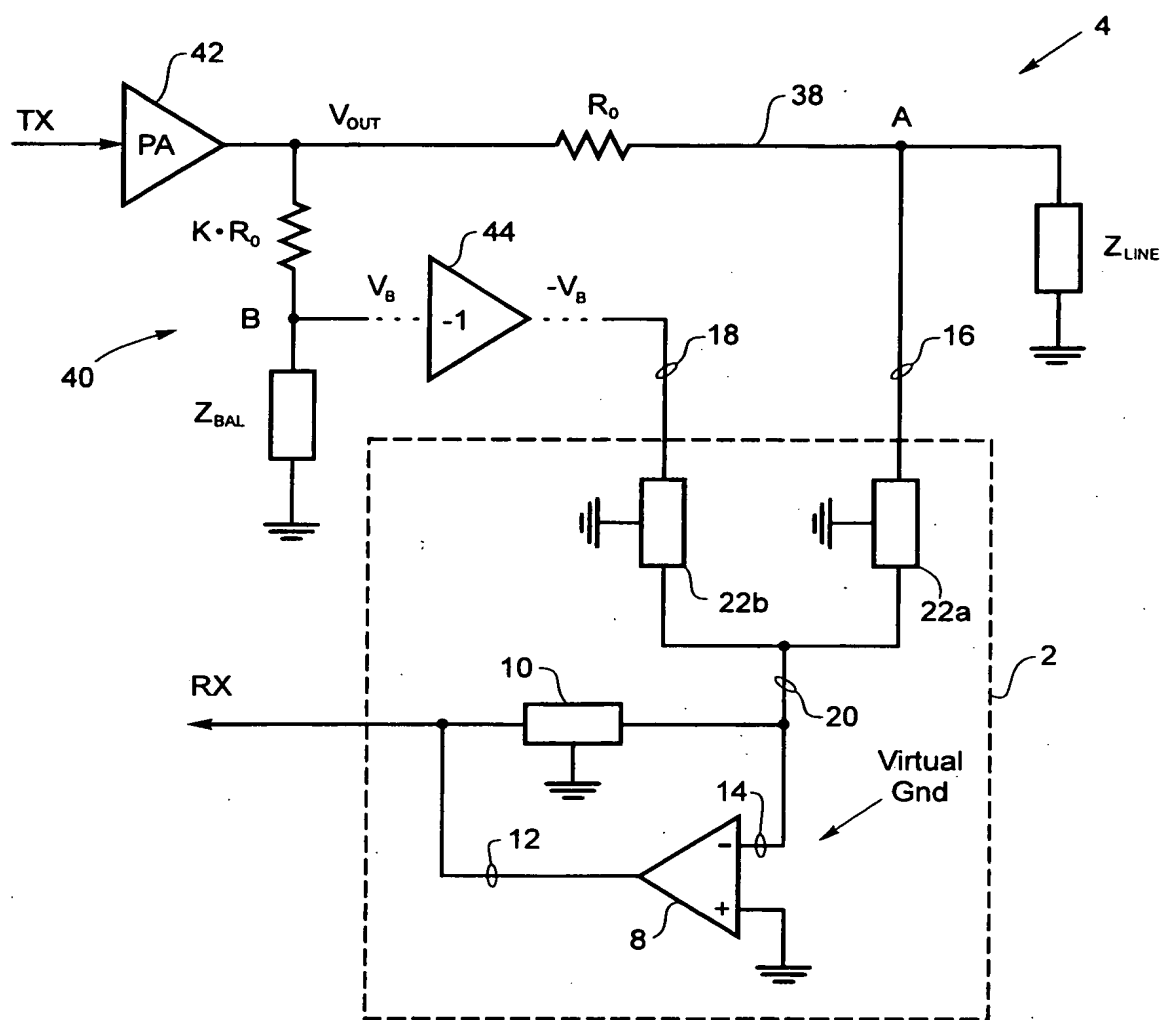


Figure 3a

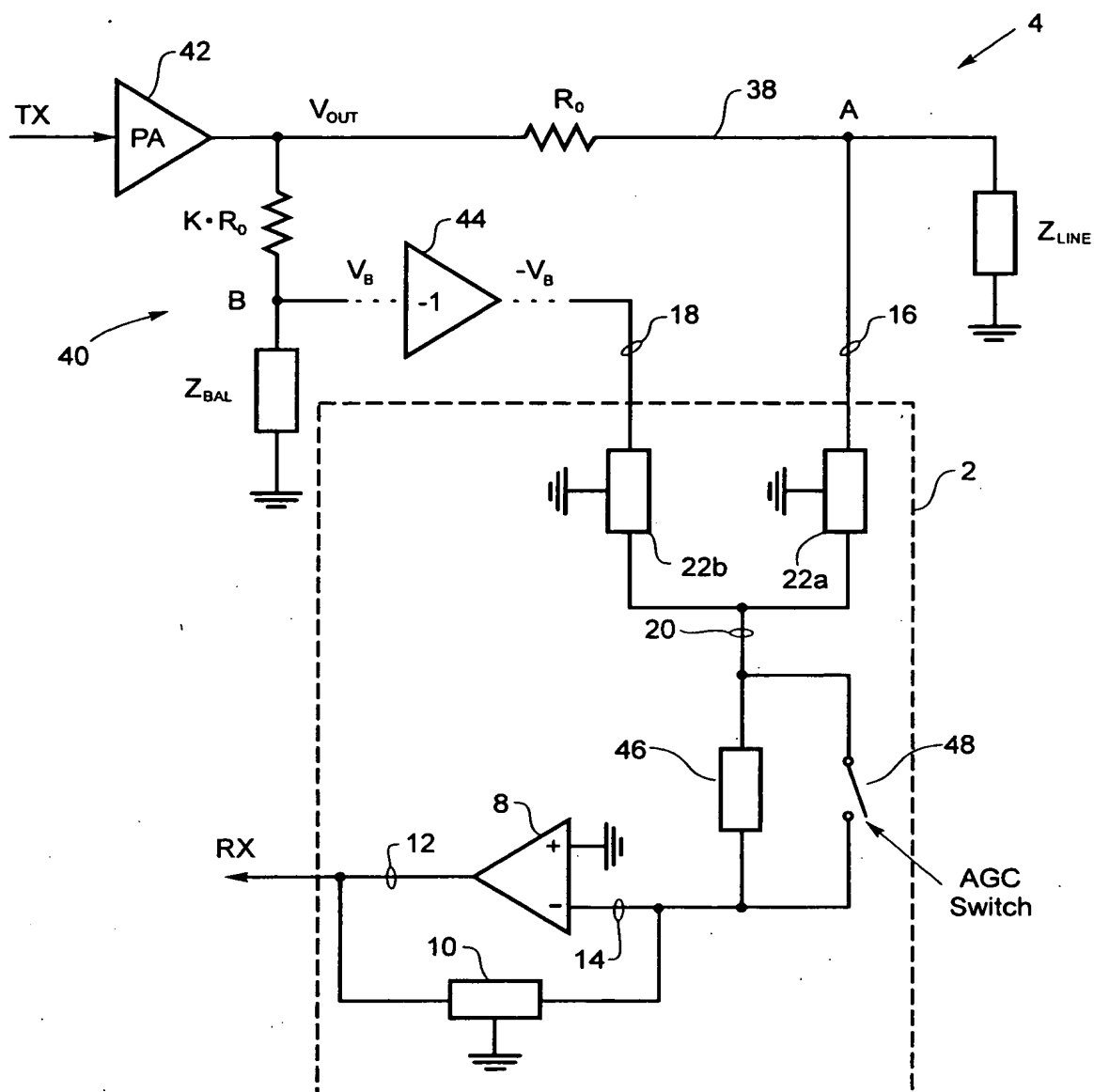


Figure 3b

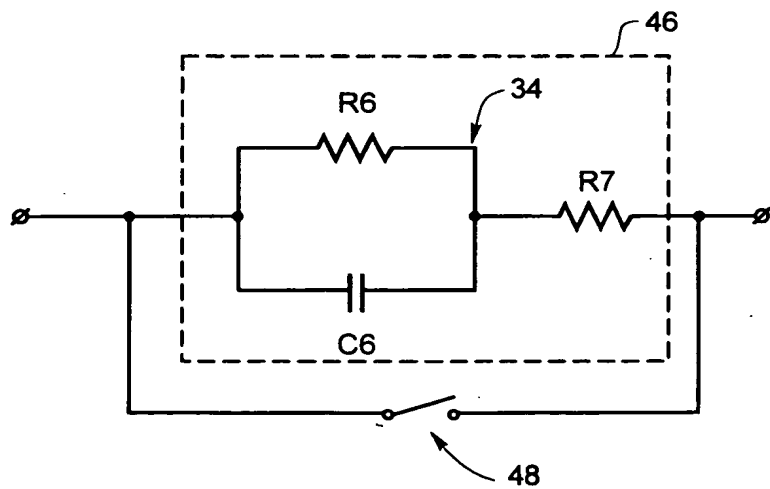


Figure 4

The diagram illustrates a balanced receiver system, labeled 4. It consists of two transmit paths (TX+ and TX-) and two receive paths (RX+ and RX-). The transmit paths use power amplifiers (42a, 42b) and a balanced network (38a, 38b) with resistors (K·R₀, R₀) and a transformer (Z_{LINE}) connected to a Local Loop. The receive paths use operational amplifiers (8) and a balanced network (2a, 2b) with resistors (22, 46) and switches (48). The system is labeled with various components and reference numerals.

Figure 5

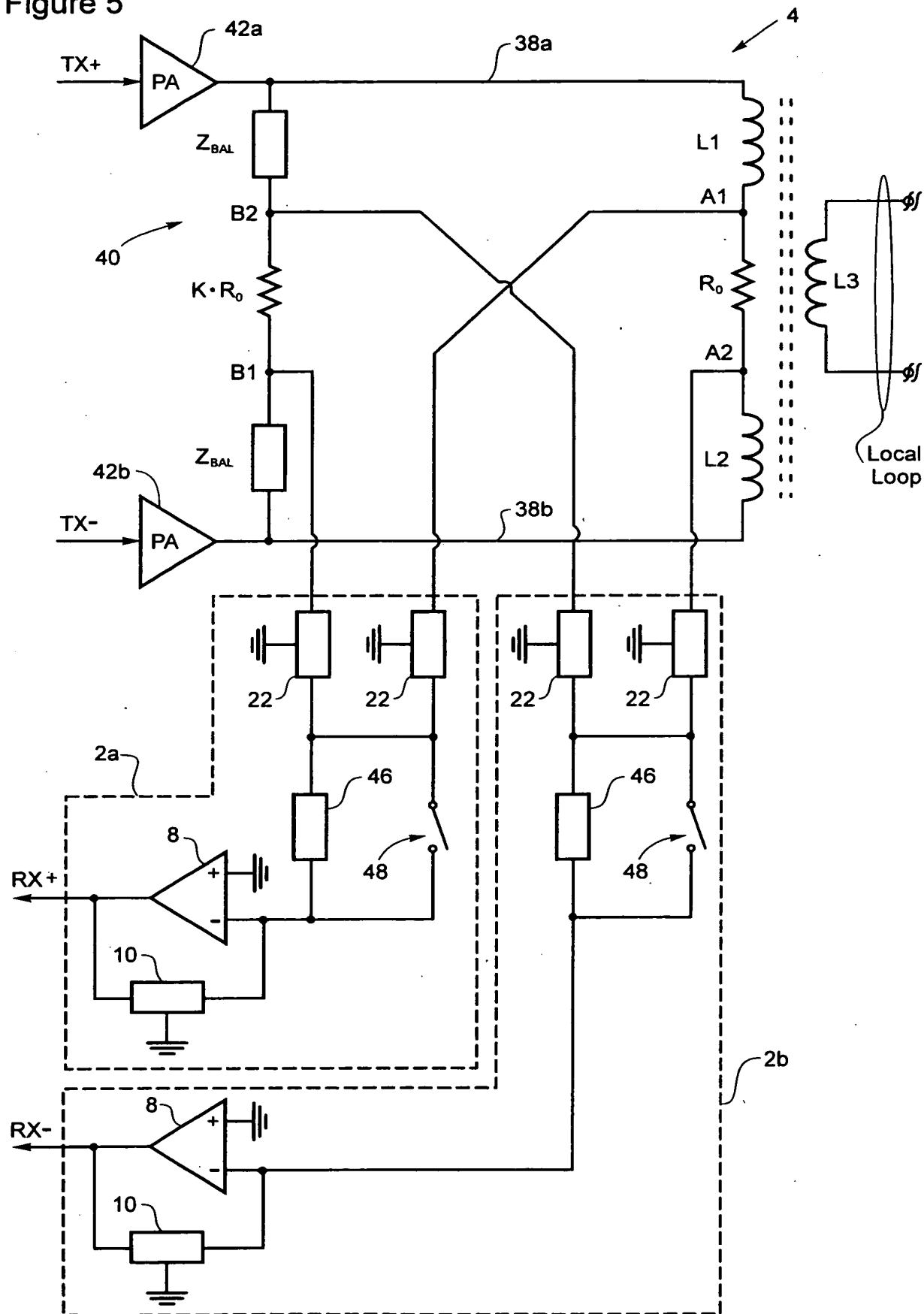


Figure 6

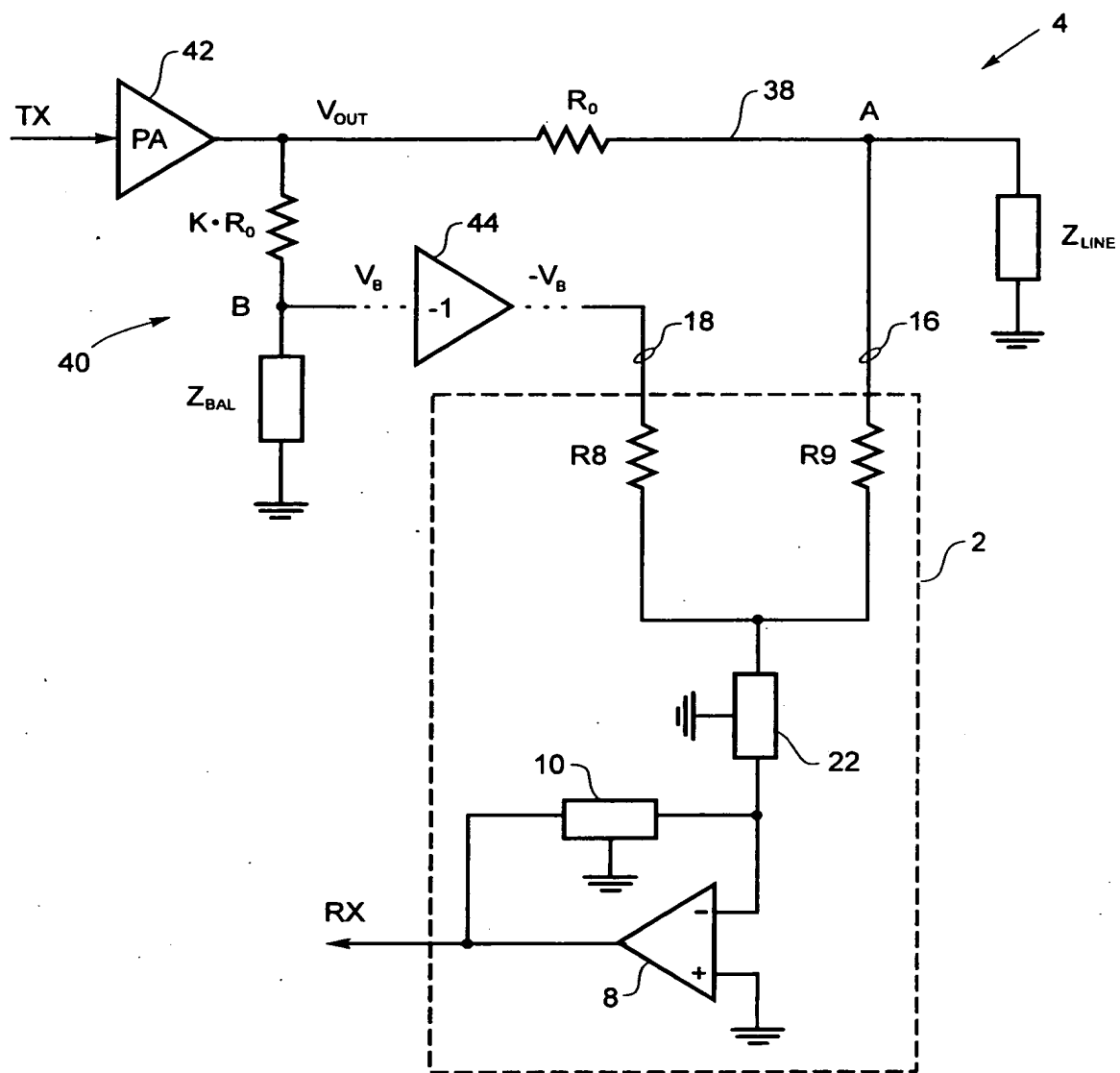


Figure 7

